

● General Description

This silicon carbide Power MOSFET device has been developed using ZMJ's advanced 2nd generation SiC MOSFET technology. The device features a very low $R_{DS(on)}$ over the entire temperature range combined with low capacitances and very high switching operations. It improves application performance in frequency, energy efficiency, system size and weight reduction.

● Features

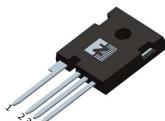
- High blocking voltage
- High speed switching with low capacitances
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low gate charge for fast switching
- Low thermal resistance
- 100% avalanche tested
- AEC-Q101 qualified

● Application

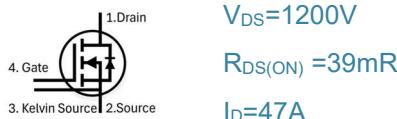
- Motor drives
- On board charger
- DC-DC
- Auxiliary drives



● Product Summary



TO-247-4

 $V_{DS}=1200V$ $R_{DS(ON)}=39m\Omega$ $I_D=47A$ 

● Ordering Information

Part NO.	ZMCSA040R120C4
Marking	ZMCS040R120
Packing information	TUBE BULK
Basic ordering unit (pcs)	450

● Absolute Maximum Ratings ($T_A=25^\circ C$, unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Max.	Unit
Drain-source voltage	V_{DS}		-	1200	V
Gate-source voltage ^①	V_{GS}	Transient Voltage	-10	25	V
	V_{GS}	Static Voltage	-10	24	V
Recommended turn on gate voltage	$V_{GS(on)}$		15	18	V
Recommended turn off gate voltage	$V_{GS(off)}$		-4	0	V
Continuous drain current	I_D	$V_{GS}=18V, T_c=25^\circ C$	-	47	A
	I_D	$V_{GS}=18V, T_c=75^\circ C$	-	39	A
	I_D	$V_{GS}=18V, T_c=100^\circ C$	-	33	A
Pulsed drain current ^①	I_{DM}	Pulsed; $t_p \leq 10 \mu s; T_c = 25^\circ C;$	-	189	A

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Total power dissipation	P _D	T _C =25°C	-	221	W
Total power dissipation	P _D	T _A =25°C	-	3.8	W
Operating junction temperature	T _J		-55	175	°C
Storage temperature	T _{STG}		-55	175	°C
Single pulse avalanche energy	E _{AS}	L=0.5mH, V _{GS} =18V, R _g =25Ω,	-	588	mJ

● Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R _{thJC}	-	-	0.68	°C/W
Thermal resistance, junction - ambient	R _{thJA} ⁽²⁾	-	-	40	°C/W
Soldering temperature	T _{sold}	-	-	260	°C

● Electronic Characteristics (T_j=25°C, unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	1200	-	-	V
Gate threshold voltage	V _{GS(th)}	V _{GS} =V _{DS} , I _D =5mA	2	2.8	4	V
Drain-source leakage current	I _{DSS}	V _{GS} =0V, V _{DS} =1200V	-	-	10	uA
Gate- source leakage current	I _{GSS}	V _{GS} =-10V, V _{DS} =0V	-	-	-100	nA
		V _{GS} =25V, V _{DS} =0V	-	-	100	nA
Static drain-source on resistance	R _{DS(ON)}	V _{GS} =18V, I _D =30A, T _j =25°C	-	39	47	mΩ
		V _{GS} =18V, I _D =30A, T _j =175°C	-	78	-	mΩ
		V _{GS} =15V, I _D =30A, T _j =25°C	-	47	-	mΩ
Forward transconductance	g _F	V _{DS} =10V, I _{SD} = 30A	-	7.5	-	S
Diode forward voltage	V _{FSD}	V _{GS} =-4V, I _{SD} = 30A	-	4.4	5	V

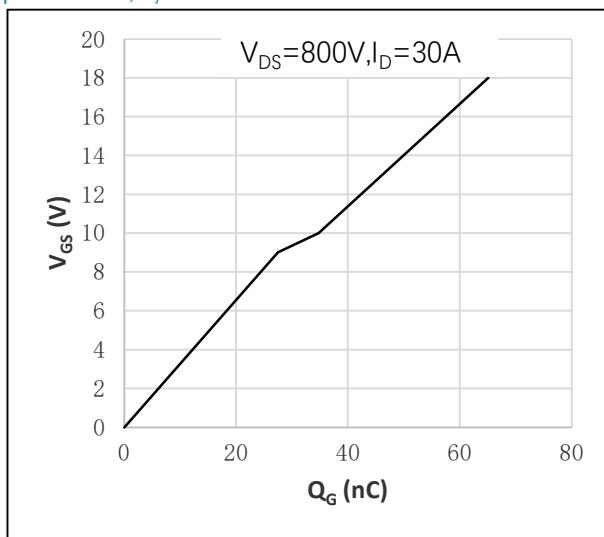
● Dynamic characteristics (T_j=25°C, unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Input capacitance	C _{iss}	f = 100KHz, V _{DS} =800V, V _{GS} =0V	-	2061	-	pF
Output capacitance	C _{oss}		-	94	-	pF
Reverse transfer capacitance	C _{rss}		-	1.6	-	pF
Output charge	Q _{oss}	f = 100KHz, V _{GS} =0V, V _{DS} =0V to 800V	-	123.3	-	nC
Coss stored energy	E _{oss}		-	34.5	-	uJ
Gate resistance	R _g	f = 1MHz	-	1.4	-	Ω

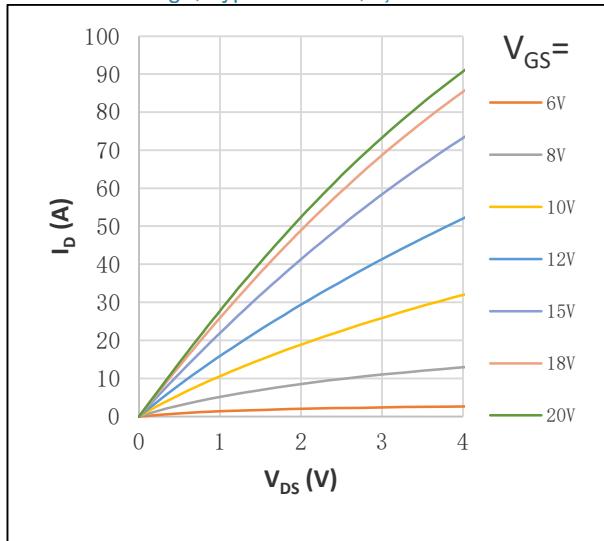
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Total gate charge	Q_g	$V_{DD} = 800V$, $I_D = 30A$, $V_{GS} = -4V/18V$	-	65.1	-	nC
Gate-source charge	Q_{gs}		-	27.5	-	nC
Gate-drain charge	Q_{gd}		-	7.3	-	nC
Turn-on delay time	$t_{D(on)}$	$V_{GS}=-4V/18V$, $V_{DS}=800V$, $R_G=30\Omega$, $I_D=20A$, $L=200\mu H$	-	32	-	ns
Turn-on rise time	t_r		-	20	-	ns
Turn-off delay time	$t_{D(off)}$		-	79	-	ns
Turn-off fall time	t_f		-	13	-	ns
Turn-on energy	E_{on}		-	356	-	uJ
Turn-off energy	E_{off}		-	286	-	uJ
Reverse recovery time	t_{rr}	$V_{DD}=800V$, $dI_s/dt =$ $890A/\mu s$, $I_s=20A$	-	36	-	ns
Reverse recovery charge	Q_{rr}		-	223	-	nC

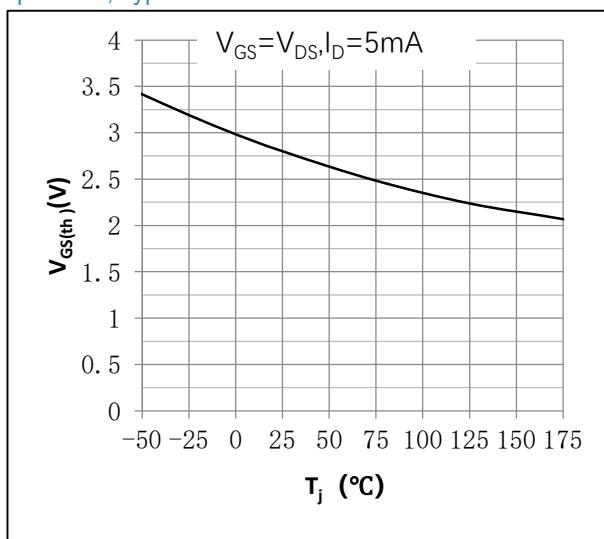
● Fig.1 Gate-source voltage as a function of gate charge; Typical values; $T_J=25^\circ\text{C}$



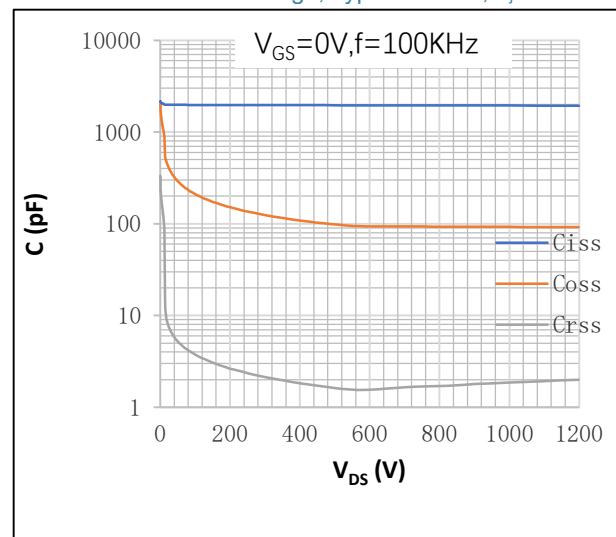
● Fig.3 Output characteristics: drain current as a function of drain-source voltage; Typical values; $T_J=25^\circ\text{C}$



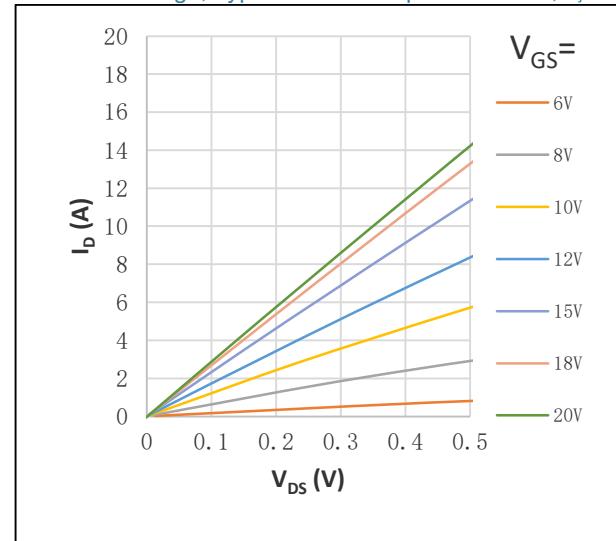
● Fig.5 Gate-source threshold voltage as a function of junction temperature; Typical values



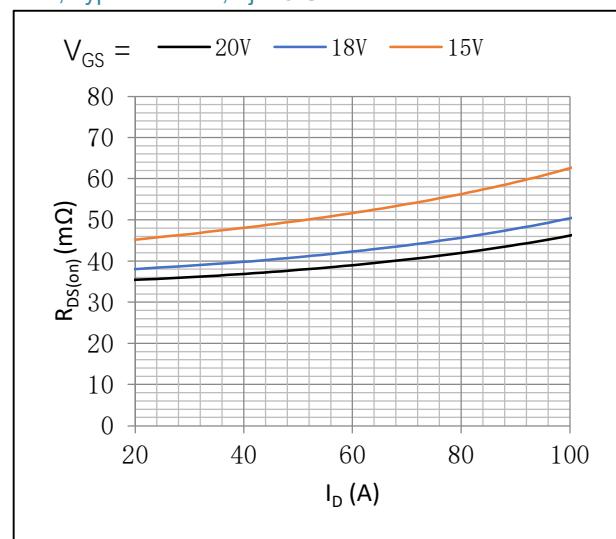
● Fig.2 Input, output and reverse transfer capacitances as a function of drain-source voltage; Typical values; $T_J=25^\circ\text{C}$



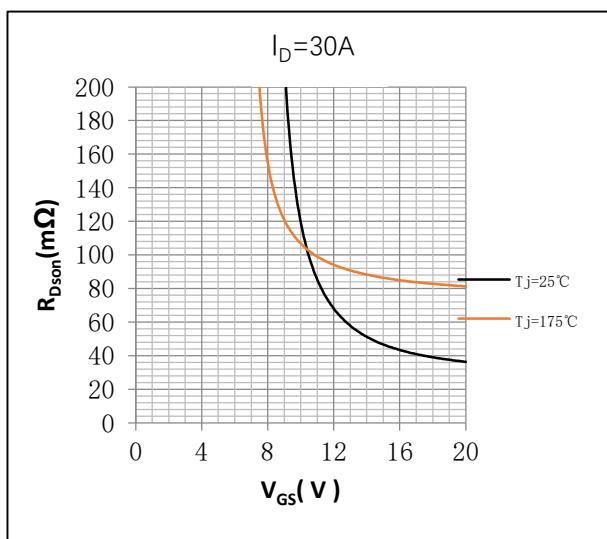
● Fig.4 Output characteristics: drain current as a function of drain-source voltage; Typical values: Expanded curve; $T_J=25^\circ\text{C}$



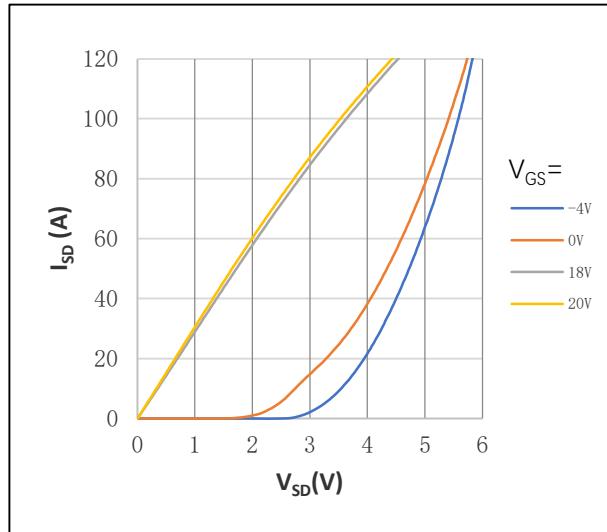
● Fig.6 Drain-source on-state resistance as a function of drain current; Typical values; $T_J=25^\circ\text{C}$



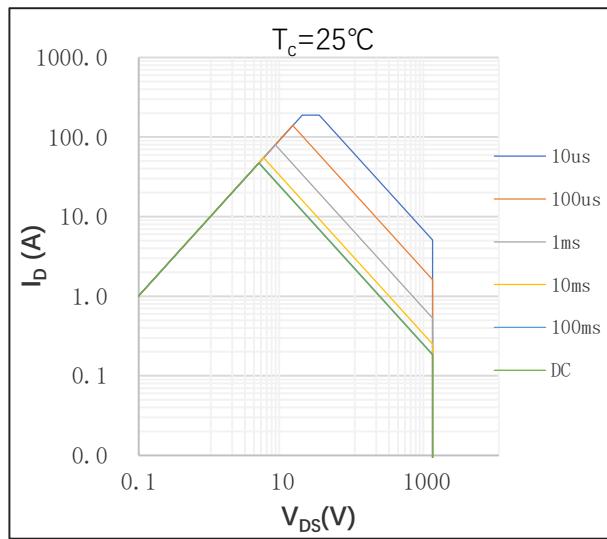
● Fig.7 Drain-source on-state resistance as a function of gate-source voltage; Typical values



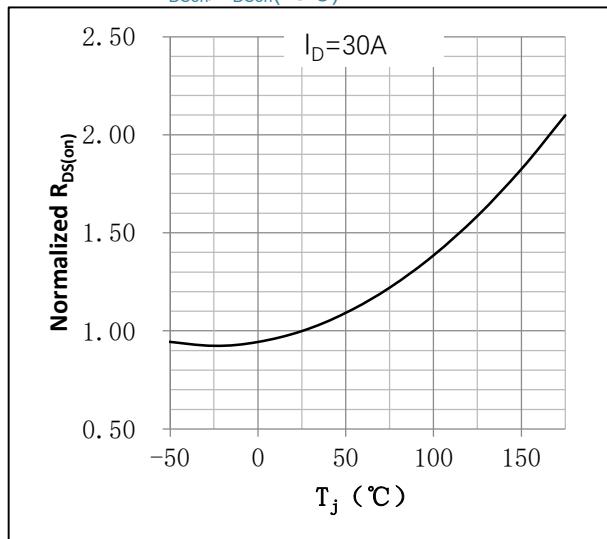
● Figure 9. Source (diode forward) current as a function of source-drain (diode forward) voltage; Typical values; T_j=25°C



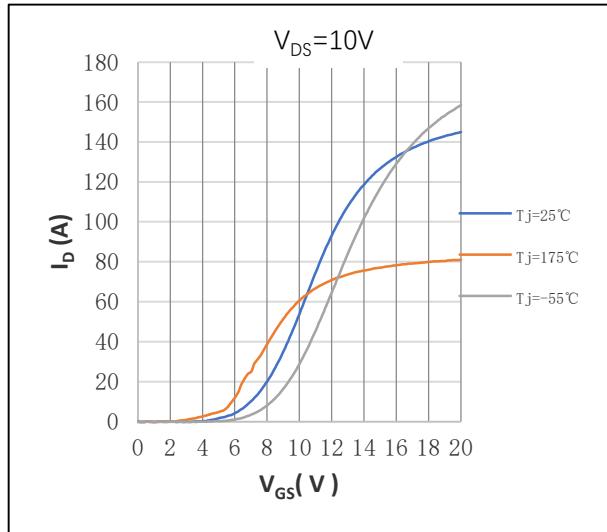
● Fig.11 Safe operating area: continuous and peak drain currents as a function of drain-source voltage; Calculative values



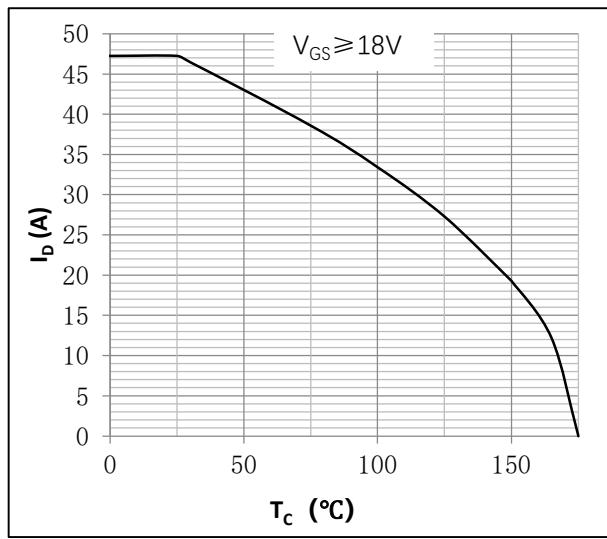
● Fig.8 Normalized drain-source on-state resistance factor as a function of junction temperature; Typical values Normalized On-Resistance=R_{DS(on)}/R_{DS(on)(25°C)}



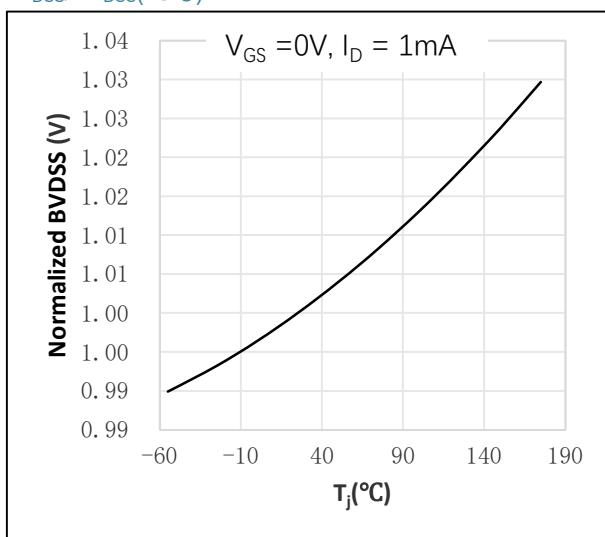
● Figure 10. Transfer characteristics: drain current as a function of gate-source voltage; Typical values



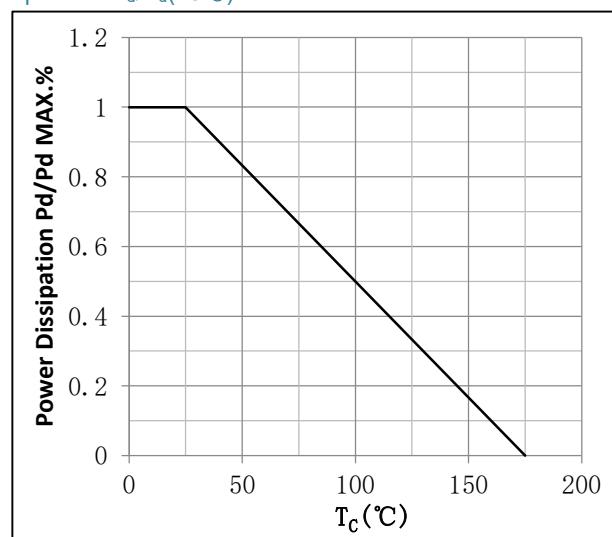
● Fig.12 Continuous drain current as a function of case temperature^③; Calculative values



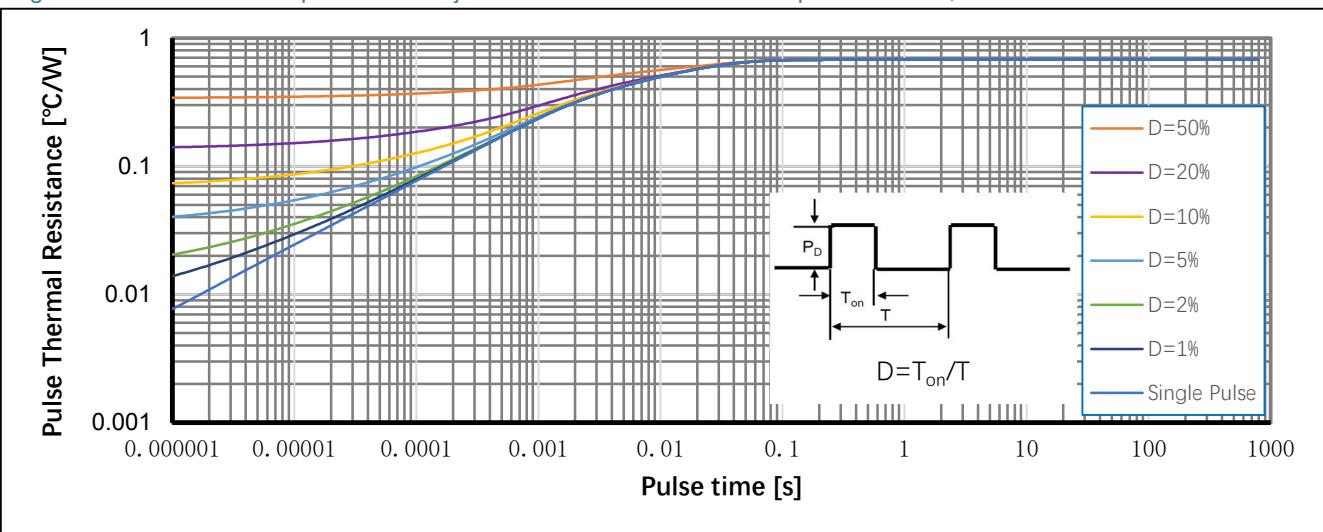
● Fig.13 Drain-source breakdown voltage as a function of junction temperature; Typical values Normalized BV_{DSS} = $BV_{DSS}/BV_{DSS}(25^\circ\text{C})$



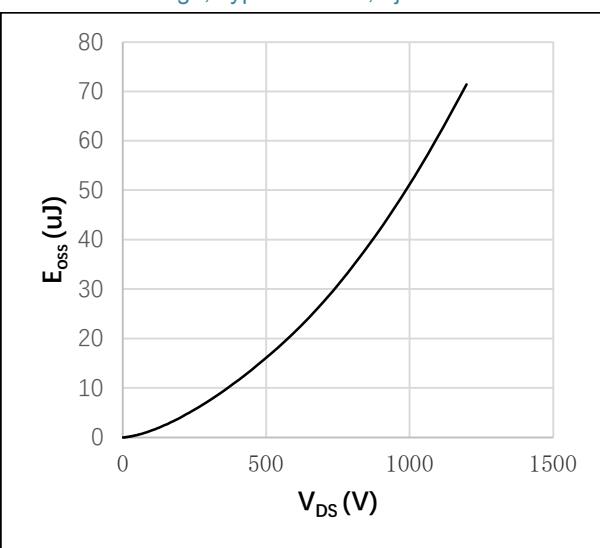
● Fig.14 Normalized total power dissipation as a function of case temperature; Calculative values Normalized Power Dissipation = $P_d/P_d(25^\circ\text{C})$



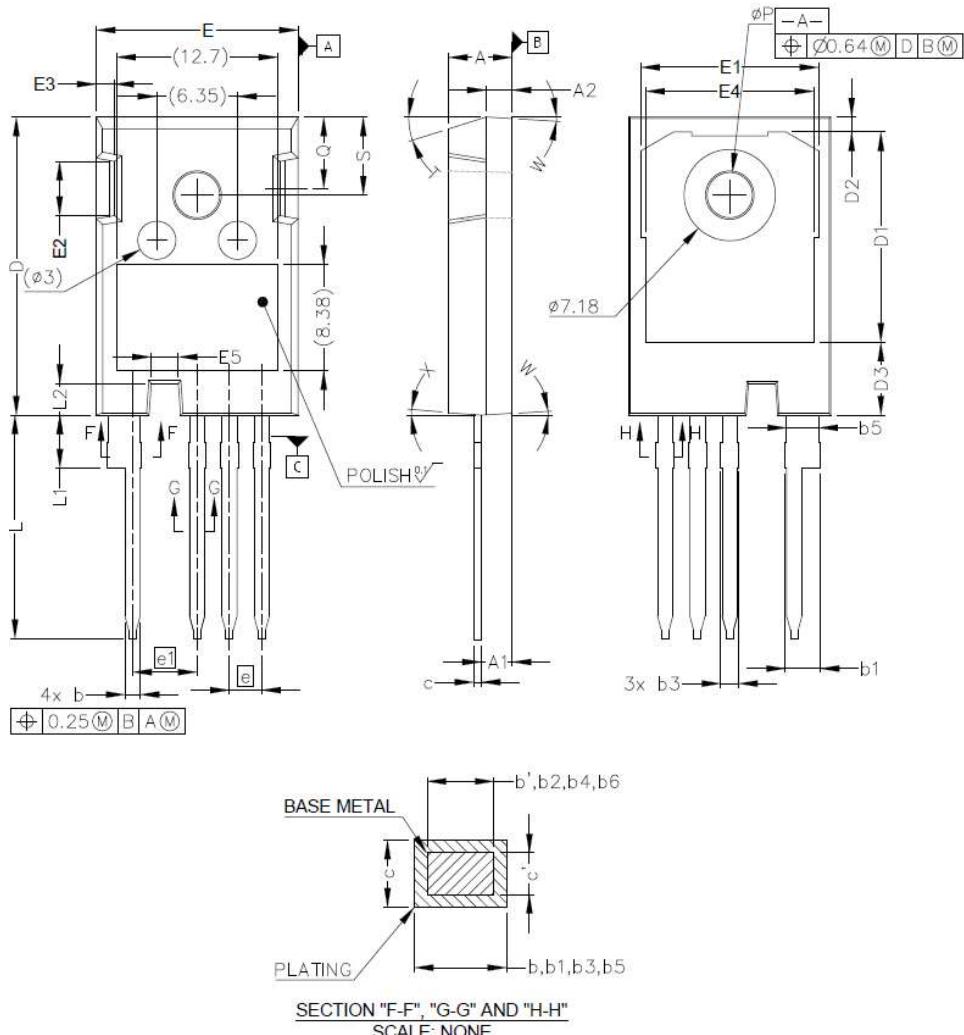
● Fig.15 Transient thermal impedance from junction to case as a function of pulse duration; max values



● Fig.16 Output capacitor stored energy as a function of drain-source voltage; Typical values; $T_j=25^\circ\text{C}$



● Package Outline



SYMBOL	MILLIMETERS	
	MIN	MAX
A	4.83	5.21
A1	2.29	2.54
A2	1.91	2.16
b'	1.07	1.28
b	1.07	1.33
b1	2.39	2.94
b2	2.39	2.84
b3	1.07	1.60
b4	1.07	1.50
b5	2.39	2.69
b6	2.39	2.64
c'	0.55	0.65
c	0.55	0.68
D	23.30	23.60
D1	16.25	17.65
D2	0.95	1.25
D3	5.55	6.15
E	15.75	16.13
E1	13.10	14.15
E2	3.68	5.10
E3	1.00	1.90
E4	12.38	13.43
E5	1.95	2.35
e	2.54	BSC
e1	5.08	BSC
N	4	
L	17.31	17.82
L1	3.97	4.37
L2	2.35	2.65
ϕP	3.51	3.65
Q	5.49	6.00
S	6.04	6.30
T	17.5°	REF.
W	3.5 °	REF.
X	4 °	REF.

● Note

① Practically the current will be limited by PCB, thermal design and operating temperature. $V_{GS}=18V$.

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● Revision History

Version	Date	Change
A	2025/8/6	New